# METHOD AND APPARATUS FOR THE AUTOMATIC CORRECTION OF FAULTY WIRES IN A LOGIC SIMULATION HARDWARE EMULATOR / ACCELERATOR

## Abstract of the Disclosure

The present invention provides a method, apparatus and program-product for a self-healing, reconfigurable logic emulation system, wherein if a signal wire becomes faulty in an emulation cable during an emulation run, the runtime software can automatically reconfigure the emulator to reroute the data destined for the faulty signal wire across a spare wire. Such a feature enables a user to restart the emulation run without having to recompile the simulation model to account for the hardware fault.